

PTO-1449

Sheet

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of

1

Complete If Known

Application Number

10/699,116

Filing Date

October 31, 2003

First Named Inventor

Donnelly, et al.

Art Unit

2189

Examiner Name

Chace, Christian

Attorney Docket Number

60809-0143-US

U.S. PATENT DOCUMENTS


[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation	
	Country Code ² - Number ³ - Kind Code ⁴ (if known)					Yes	No
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OTHER NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

of 4

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Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5742840	04-21-1998	Hansen et al.	
		US-5748872	05-05-1998	Norman	
		US-5860080	01-12-1999	James et al.	
		US-5867422	02-02-1999	John	
		US-5898863	04-27-1999	Ofer et al.	
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		US-6038682	03-14-2000	Norman	
		US-6092229	07-18-2000	Boyle et al.	
		US-6125419	09-26-2000	Umemura et al.	
		US-6151648	11-21-2000	Haq	
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		US-6487102	11-26-2002	Halbert et al.	

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Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt	
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		NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39	
		SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42	

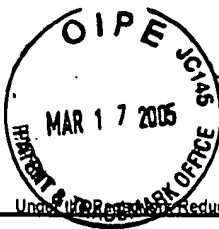
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Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-6016282	1-18-2000	Keeth	
		US-6449727	09-10-2002	Toda	
		US-4644532	02-17-1987	George et al.	
		US-4667305	05-19-1987	Dill et al.	
		US-4747100	05-24-1988	Roach et al.	
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		US-5228134	07-13-1993	MacWilliams et al.	
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		US-5544342	08-06-1996	Dean	
		US-5553266	09-03-1996	Metzger et al.	
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		US-5659710	08-19-1997	Sherman et al.	
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 99/30240	06-17-1999	Intel Corporation		
		WO 99/41666	08-19-1999	Intel Corporation		
		WO 99/41667	08-19-1999	Intel Corporation		
		EP 1069509 A2	01-17-2001	Texas Instruments Incorporated		

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Sheet 2

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		Number-Kind Code ² (if known)			
		US-5802565	09-01-1998	McBride	
		US-5805798	09-08-1998	Keams et al.	
		US-5848145	12-08-1998	Gallagher et al.	
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		US-6317352	11-13-2001	Halbert et al.	
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		US-6493250	12-10-2002	Halbert et al.	
		US-6530006	03-04-2003	Dodd et al.	
		US-6449679	09-10-2002	Ryan et al.	
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		US-6208273	03-27-2001	Dye et al.	
		US-5883839	03-16-1999	Tosaka et al.	

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		US-20040250181	12-09-2004	Vogt et al.	
		US-20040250153	12-09-2004	Vogt	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 5 of 7

Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
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		"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
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		JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

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				Filing Date	October 31, 2003
				First Named Inventor	Donnelly
				Art Unit	2187
				Examiner Name	Chace, C.
Sheet	6	of	7	Attorney Docket Number	RAMB-01065US1

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		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004		

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Sheet 7 of 7

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